Design with the Zynq™ UltraScale+ RFSoC

3 days - 21 hours

OBJECTIVES

- After completing this training, you will have the skills to:
  - 1 - Describe the RFSoC family in general
  - 2 - Identify applications for RF Data Converter blocks
  - 3 - Configure, simulate and implement the blocks
  - 4 - Verify RF Data Converters on real hardware

PREREQUISITES

- Understanding of the Zynq™ MPSoC architecture
- Basic familiarity with data converter terms and principles

CONCERNED PUBLIC

- Technicians and Engineers in Digital Electronics
- All our training courses are given at a distance and are accessible to people with reduced mobility.
- Our partner AGEFIPH accompanies us to implement the necessary adaptations related to your disability.

NOTES

- Release date: 20/12/2021

CHAPTERS

DAY 1

- Objective 1
  - Zynq UltraScale+ RFSoC Overview {Lecture, Demo}
- Objective 2
  - RF-ADC Hardware {Lecture, Demo, Lab}

DAY 2

- Objective 2
  - RF-DAC Hardware {Lecture, Demo, Lab}
  - RFSoC Hardware {Lecture, Demo}
- Objective 3
  - Data Converter Design {Lecture, Demo, Lab}

DAY 3

- Objective 4
  - Practice on ZCU111 {Lecture, Demo, Lab}

TEACHING METHODS

- Inter-company training :
  - Online training
  - Provision of course material in PDF format
  - Loan of a ZCU111 card

METHODS OF MONITORING AND ASSESSMENT OF RESULTS

- Attendance sheet
- Evaluation questionnaire
- Evaluation sheet on:
  - Technical questionnaire
  - Result of the Practical Works
  - Validation of Objectives
- Presentation of a certificate with assessment of prior learning

SUPPORT

- Authorized Trainer Provider AMD : Engineer Electronics and Telecommunications ENSIL
  - Expert AMD FPGA - Language VHDL/Verilog - RTL Design
  - Expert AMD SoC & MPSoC - Language C/C++ - System Design
  - Expert DSP & AMD RFSoC – HLS - Matlab - Design DSP RF
  - Expert AMD Versal – AI Engines – Heteregenous System Architect

PC RECOMMENDED

- Software Configuration :
  - Vitis Unified Software Platform 2021.1
- Hardware configuration:
  - Recent computer (i5 or i7)
  - OS Linux 64-bits (Windows 10 compatible)
  - At least 16GB RAM
  - Display resolution recommended 1920x1080
  - Zynq UltraScale+ RFSoC ZCU111 board (will be lent)

PARTNERS

AMD
Authorized Training Provider

CONTACT

Administratif / Formateur : (+33) 06 74 52 37 89
info@mvd-training.com

Qualiopi processus certifié
RÉPUBLIQUE FRANÇAISE
La certification a été délivrée au titre de la catégorie d'action suivante :
ACTIONS DE FORMATION